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10/072,145

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SEP 15 2004

In re Application of: Averett, Guy E. et al.
Serial No.: 10/072,145
Filing Date: 02/07/2002
Group Art Unit: 2811
Examiner: Magee, Thomas J.
Title: SEMICONDUCTOR DEVICE AND METHOD OF
PROVIDING REGIONS OF LOW SUBSTRATE
CAPACITANCE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE
TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE, ATTN: EXAMINER
Thomas J. Magee AT THE FAX NO. (703) 872-9306

ON: 9/15/04
Date of Facsimile Transmission
Kevin B. Jackson
Name of Person Signing Certificate
 9/15/04
SIGNATURE DATE

Honorable Commissioner of Patents,
P.O. BOX 1450
Alexandria, VA 22313-1450

AMENDMENT

SIR:

In response to the office action mailed June 16, 2004,
please reconsider the above-identified application in view
of the amendments and remarks presented hereinafter